

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5590	((level adj1 conver\$4) or (level adj1 shift\$3) or (level adj1 translat\$3)) and transistor\$1 and ((bias NEAR1 circuit) or (bias NEAR1 generat\$3) or (reference NEAR1 circuit) or (reference NEAR1 generat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 17:06
L2	541	1 and (input NEAR1 source)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 17:08
L3	171	2 and (gate NEAR2 (vdd or vc or vcc or supply))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 17:20
L4	0	3 and (degree NEAR1 (on or active or switch\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 17:22
L5	97	3 and ((low adj1 level) and (high adj1 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/10 17:25

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12	(((level adj1 conver\$4) or (level adj1 translat\$3) or (level adj1 shift\$3)) and ((first adj2 type) or (p-channel adj2 type) or pfet or pmos) and ((second adj2 type) or (n-channel adj2 type) or nfet or nmos) and ((bias adj1 circuit) or (reference adj1 circuit) or (bias adj1 generat\$3) or (reference adj1 generat\$3))).clm.	US-PGPUB	OR	ON	2006/02/10 17:33